## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Number	10/826435	Docket Number	CNTR.2075			
Filed	04/16/04	Group Art Unit	2132			
Examiner	GILBERTO BARRON JR.	Customer No.	23669			
Application Title	MICROPROCESSOR APPARATUS AND METHOD FOR PROVIDING CONFIGURABLE CRYPTOGRAPHIC BLOCK CIPHER ROUND RESULTS					
First Named Inventor	G. GLENN HENRY					

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571-273-8300

From:

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Fax Number:

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Respectfully submitted, HUFFMAN PATENT GROUP, LLC

/Richard K. Huffman/

RICHARD K. HUFFMAN, P.E.

Registration No. 41,082 Tel: (719) 575-9998

06/04/2006

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#### INFORMATION DISCLOSURE STATEMENT TRANSMITTAL

VIA FACSIMILE: 571-273-8300

Mail Stop Commissioner for Patents PO Box 1450 Alexandria, VA 22313-1450

Dear Sir:

Attached hereto is Form PTO-1449 listing documents believed relevant to the subject application. It is respectfully requested that the examiner review the information disclosed herein in detail, independently evaluate each item carefully in the consideration of the pending claims, and return an initialed copy of each form to the undersigned practitioner.

This disclosure statement should not be construed as a representation that a search has been made, that no other material information as defined in 37 CFR 1.56(a) exists, or as an admission that the information cited in the statement is, or is considered to be, material to patentability as defined in 37 CFR 1.56(b) or is available as a reference under 35 U.S.C. §102 et seg. Applicant reserves the right to swear behind or otherwise disprove the allege "prior" nature of any art cited should the facts support and the situation warrant such an action.

It is believed that this disclosure complies with the requirements of 37 CFR 1.56, 1.97, and 1.98, and the Manual of Patent Examining Procedures \$609. If for some reason the examiner considers otherwise, it is respectfully requested that the undersigned be called so that any deficiencies can be remedied.

Some of the enclosed documents may have markings thereon. No significance is intended to be attached to the markings.

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	Respectfully submitted, HUFFMAN PATENT GROUP, LLC						
Bv:	/Richard K. Huffman/						
<i>,</i> –	RICHARD K. HUFFMAN, P.E. Registration No. 41,082 Tel: (719) 575-9998						
	06/04/2006						
Date:		_					

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Substitute for form 1449A/PTO				Complete if Known		
Substitute for form 1449AV	P10			Application Number	10/826435	
INFORMATION DISCLOSURE				Filing Date	04/16/04	
STATEMENT BY APPLICANT  (use as many sheets as necessary			ANT	First Named Inventor	G. Glenn Henry	
				Group Art Unit	2132	
				Examiner Name	Gilberto Barron Jr.	
Sheet	1	of	2	Attorney Docket Number	CNTR.2075	

	U.S. PATENT DOCUMENTS						
Examiner Cite	Document Number	Publication Date	Name of Patentee or	Pages, Columns, Lines, Where			
Initials*		MM-DD-YYYY	Applicant of Cited Document	Relevant Passages or Relevant Figures Appear			
		2002191784	12-19-2002	Yup et al.			
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FOREIGN PATENT DOCUMENTS							
Examiner Cite		Foreign Patent Document	Publication Date	Name of Patentee or	Pages, Columns, Lines,	76	
Initials* No. Country Code <sup>3</sup> - Number <sup>4</sup> - Kind Code <sup>5</sup> (# Anown)	MM-DD-YYYY	Applicant of Cited Document	Where Relevant Passages or Relevant Figures Appear	L			
	AA	EP1271839	01-02-2003	Okada et al.		L	
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					2132	
(use as many sheets as necessary				Examiner Name	Gilberto Barron Jr.	
Sheet	2	of	2	Attorney Docket Number	CNTR.2075	

		NON PATENT LITERATURE DOCUMENTS	
Examiner Cite No.1		Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²
	ВА	WU et al. "CryptoManiac: a fast flexible architecture for secure communication." Proceedings of the 28th. International Symposium on Computer Architecture. ISCA 2001. 06-30-2001 pages 104-113, XP010553867	
	BB	GLADMAN, BRIAN. "A Specification for Rijndael, the AES Algorithm." Online. 03/13/2003. XP002376287	
	вс	BACKHUS E. "AES in FPGAS Implementierung Des Advanced Encryption Standards in Hardware." Elektronik, WEKA Fachzeitschriftenverlag, Poing, DE. Vol. 52, no.8. 04/15/2003 pages 54-56, 58, 60. XP001160048	
	BD	DAEMON J et al. "AES Proposal: RIJNDAEL." AES Proposal 09/03/1999. pages 1-45, XP001060386	
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